

IN THE CLAIMS:

Please amend claim 1 as follows.

1. (Currently Amended) A network switch stack configuration, said configuration comprising:

a first network switch comprising a plurality of data ports, a first stacking port, and a first CPU interface;

a second network switch having a plurality of data ports, a second stacking port, and a second CPU interface; and

B a common CPU connected to said first CPU interface and said second CPU interface;

wherein the first stacking port and the second stacking port are communicatively connected, such that incoming packets on any of the plurality of data ports on the first and second switches can be effectively switched to any of the plurality of data ports on either of the first and second network switches; and

wherein the first and second switches add module headers having module identifier fields to the incoming packets and the first and second stacking ports read the module headers to determine egress ports for the packets.

2. (Original) A network switch stack configuration as recited in claim 1, wherein said common CPU is configured to program functions on the first and second

network switch, and wherein the common CPU controls communication between the first and second network switch.

3. (Original) A network switch stack configuration as recited in claim 1, wherein each of said first and second stacking ports include an arbiter thereupon, for allocating communication bandwidth between the first and second stacking port.

4. (Original) A network switch stack configuration as recited in claim 1, wherein each of said first and second stacking ports includes flow control logic for controlling data flow to and from each of the first and second network switches, respectively.

5. (Original) A network switch stack configuration as recited in claim 3, wherein each arbiter is configured to alternate bandwidth access by alternating transmission and reception of data based upon a predetermined algorithm.

6. (Previously Presented) A network switch stack configuration as recited in claim 1, wherein the first and second stacking ports are configured to forward the packets to the egress ports without requiring a lookup in an address resolution table.

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